AMENDMENTS TO THE CLAIMS:

Please cancel claims 1-6 without prejudice. Kindly amend claims 7 and 8, as shown below. Please add new claims 9-12, as shown below.

This listing of claims will replace all prior versions and listings of claims in the Application:

Claim 1-6 (cancelled)

Claim 7 (current amended): A variable drive current driver circuit, comprising:

a pair of push-pull circuits for driving a load circuit complementarily;

a first current source circuit for having a first bias current flow into flowed to said pair of push-pull circuits;

a second current source circuit for having said first bias current flow out of flowed from said pair of push-pull circuits;

a third current source circuit capable of having a second bias current flow into flowed to said pair of push-pull circuits;

a fourth current source circuit capable of having said second bias current flow-out

of flowed from said pair of push-pull circuits; and

a control circuit for varying both said second bias current flowed by said third current source circuit and said second bias current flowed by said fourth current source circuit according to a control signal.

Claim 8 (currently amended): The variable drive current driver circuit according to claim 7, wherein

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said control circuit has said third current source circuit have said second bias current flow flowed nor or not flow flowed into to said pair of push-pull eircuit circuits, and

said control circuit has said fourth current source circuit have said second bias current flow flowed or not flow flowed out of from said push-pull eircuit circuits.

Claim 9 (new): A variable drive current driver circuit, comprising:

a pair of push-pull circuits for driving a load circuit complementarily;

a first current source circuit for having a first bias current flowed to said pair of push-pull circuits;

a second current source circuit for having said first bias current flowed from said pair of push-pull circuits;

a third current source circuit capable of having a second bais current flowed to said pair of push-pull circuits;

a fourth current source circuit capable of having said second bias current flowed from said pair of push-pull circuits; and

a control circuit for varying both said second bias current flowed by said third current source circuit and said second bias currently flowed by said fourth current source circuit according to a control signal,

wherein each push-pull circuit comprises at least two gate types.

Claim 10 (new): The variable drive current driver circuit according to claim 9, wherein said control circuit has said third current source circuit have said second bias current flowed or not flowed to said pair of push-pull circuits, and

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said control circuit has said fourth current source circuit have said second bias current flowed or not flowed from said push-pull circuits.

Claim 11 (new): A variable drive current driver circuit, comprising:

a pair of push-pull circuits for driving a load circuit complementarily;

a first current source circuit for having a first bias current flowed to said pair of push-pull circuits;

a second current source circuit for having a second bias current flowed from said pair of push-pull circuits;

a third current source circuit capable of having a third bias current flowed to said pair of push-pull circuits;

a fourth current source circuit capable of having a fourth bias current flowed from said pair of push-pull circuits; and

a control circuit for varying both said third bias current flowed by said third current source circuit and said fourth bias current flowed by said fourth current source circuit according to a control signal.

Claim 12 (new): The variable drive current driver circuit according to claim 5, wherein said control circuit has said third current source circuit have said third bias current flowed or not flowed to said pair of push-pull circuits, and

said control circuit has said fourth current source circuit have said fourth bias current flowed or not flowed from said push-pull circuits.

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